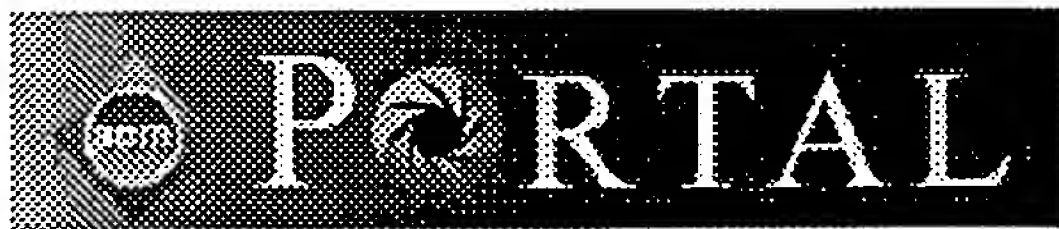


Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	23713	"710"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:53
L2	46	I1 and (((disabl\$4 or deactivat\$4 or disconnect\$4 or de\$power\$4 or prohibit\$4 or inhibit\$4)near5 (power or voltage or supply))with (bus or USB or (serial near4 interface)))same (presen\$4 or detect\$4 or recogniz\$4 or identif\$4)with (voltage or signal or self\$power\$4 or ((self or source)near3 power\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:55
L3	22268	"713"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:43
L4	8	I2 and ((protect\$4 or prevent\$4)with damage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:45
L5	44	I3 and (((disabl\$4 or deactivat\$4 or disconnect\$4 or de\$power\$4 or prohibit\$4 or inhibit\$4)near5 (power or voltage or supply))with (bus or USB or (serial near4 interface)))same (presen\$4 or detect\$4 or recogniz\$4 or identif\$4)with (voltage or signal or self\$power\$4 or ((self or source)near3 power\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:44
L6	6	I5 and ((protect\$4 or prevent\$4)with damage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 15:04

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L9	3	18 and (((protect\$4 or prevent\$4)with damage)same (peripheral or I/O))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 15:05

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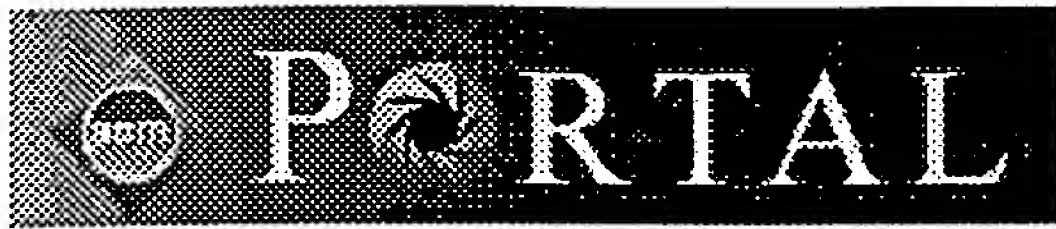
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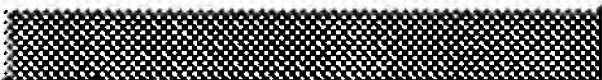
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Phadke, A.G.; Pickett, B.; Adamiak, M.; Begovic, M.; Benmouyal, G.; Burnett, R.O., Jr.; Cease, T.W.; Goossens, J.; Hansen, D.J.; Kezunovic, M.; Mankoff, L.L.; McLaren, P.G.; Michel, G.; Murphy, R.J.; Nordstrom, J.; Sachdev, M.S.; Smith, H.S.; Thorp, J.S.; Trotignon, M.; Wang, T.C.; Xavier, M.A.;
Power Delivery, IEEE Transactions on
Volume 9, Issue 1, Jan. 1994 Page(s):442 - 452
Digital Object Identifier 10.1109/61.277716
[AbstractPlus](#) | Full Text: [PDF\(1172 KB\)](#) IEEE JNL
- ☐ 2. Refine and gabriel: support for refinement and tactics
Oliveira, M.; Xavier, M.; Cavalcanti, A.;
Software Engineering and Formal Methods, 2004. SEFM 2004. Proceedings of the Second International Conference on
28-30 Sept. 2004 Page(s):310 - 319
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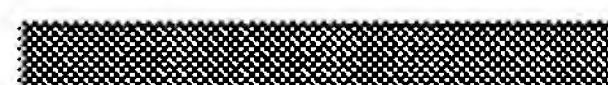
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 Kramer, A.; Fabbizio, V.; Mariaud, X.; Raynal, F.;
 Solid-State Circuits Conference, 1998. Digest of Technical Papers. 45th ISSCC 1998 IEEE International
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 Digital Object Identifier 10.1109/ISSCC.1998.672433
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- ☐ 2. **Low power, low voltage conductance-mode CMOS analog neuron**
 Fabbizio, V.; Raynal, F.; Mariaud, X.; Kramer, A.; Colli, G.;
 Microelectronics for Neural Networks, 1996., Proceedings of Fifth International Conference on
 12-14 Feb. 1996 Page(s):111 - 115
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